

CRYSTAL OSCILLATOR (SPXO)

OUTPUT: LV-PECL, LVDS





Product Number

SG2520EGN: X1G005881xxxx15 SG2520VGN: X1G005901xxxx15

SG2520EGN SG2520VGN

•Frequency range : 25 MHz to 500 MHz

 Supply voltage : 1.8 V Typ. (LVDS only) / 2.5 V Typ. / 3.3 V Typ.

: $\pm 25 \times 10^{-6}$, $\pm 30 \times 10^{-6}$, $\pm 50 \times 10^{-6}$ •Frequency tolerance •Operating temperature: -40 °C to +85 °C, -40 °C to +105 °C Output enable (OE) or Standby (\overline{ST}) Function : 50 fs Max. (fo = 491.52 MHz) Phase jitter



 $(2.5 \times 2.0 \times 0.74 \text{ mm})$

Specifications (characteristics)

			Specifications				
Item	Symbol	LV-PECL			Conditions / Remarks		
		SG2520EGN	SG2	520VGN			
Output frequency range	fo		25 MHz to 500 M	Hz	Please contact us for ava	ailable frequencies.	
Supply voltage	Vcc	D: 2.5 V ± 5 % C: 3.3 V ± 5 %	E: 1.8 V ± 5 %	D: 2.5 V ± 5 % C: 3.3 V ± 5 %			
Storage temperature	T_stg		-55 °C to +125 °	C			
Operating temperature	T_use	G:	-40 °C to +85 °C, H: -40	°C to +105 °C			
		D: ±25 × 10 ⁻⁶ Max.			Includes initial frequency	tolerance, frequency /	
Frequency tolerance	f_tol	E: ±30 × 10 ⁻⁶ Max.		temperature characterist			
			J: ±50 × 10 ⁻⁶ Max.			coefficient and 10 years aging (+25 °C)	
		60 mA Max.	_		OE or $\overline{ST} = V_{CC}$, L_ECL		
Current consumption	Icc			25 mA / 30 mA / 25 mA Max.	OE or $\overline{ST} = V_{CC}$.	25 MHz ≤ fo < 212 MHz	
Current concumption	100	_		28 mA / 35 mA / 28 mA Max.	Output option: A / B / C	212 MHz ≤ fo < 392 MHz	
				28 mA / 35 mA / 30 mA Max.		392 MHz ≤ fo ≤ 500 MHz	
Disable current	I_dis	35 mA Max.		nA Max.	OE = GND		
Stand-by current	I std		30 μA Max.		ST = GND, T_use Max.		
	_		60 μA Max.		ST = GND, T_use Max. = +105 °C		
Symmetry	SYM	\/ 4 4 \/ 1 4	45 % to 55 %		At output crossing point		
Output voltage (LV-PECL)	VoH	V _{CC} - 1.1 V Min.	_		Output option: A, DC characteristic		
,	VoL	V _{CC} - 1.5 V Max. 0.8 V to 2.0 V	500 mV to 900 mV				
Differential swing	V _{sw}			400 mV to 1 000 mV	Output option: A Output option: B		
Differential Swing	VSW			to 1 200 mV	Output option: C		
	V _{OD}	_	250 mV to 450 mV		Output option: A		
		-		200 mV to 500 mV	Output option: B	Differential output voltage,	
				/ to 600 mV	Output option: C	V _{OD1} , V _{OD2}	
Output voltage (LVDS)	dV _{OD}	_			dV _{OD} = V _{OD1} - V _{OD2}		
	Vos	_	0.65 V to 0.85 V	1.15 V to 1.35 V	Offset voltage, Vos1, Vos	2	
	dVos	_		nV Max.	$dV_{OS} = V_{OS1} - V_{OS2} $	-	
	L ECL	50 Ω			Terminated to V _{CC} - 2.0 \	/	
Output load condition	_		100 Ω		Output option: A, C	Connected between	
· ·	L_LVDS	_	50 Ω		Output option: B	OUT and OUT	
Innut voltage	V _{IH}		70 % V _{CC} Min.		OE or ST terminal		
Input voltage	VIL	30 % V _{CC} Max.		OE or ST terminal			
Rise/Fall times	4/46	0.35 ns Max.		LV-PECL: 20 % - 80 %	(V _{OH} - V _{OL})		
Rise/Fail times	tr/tf			LVDS: 20 % - 80 %	differential output peak to peak		
Start-up time	t_str		10 ms Max.		t = 0 at 90 % Vcc		
		250 fs Max.	400 fs Max.	250 fs Max.	25 MHz ≤ fo < 100 MHz	Offset frequency	
		90 fs Max.	130 fs Max.	100 fs Max.	100 MHz ≤ fo ≤ 156 MHz		
Phase jitter	t₽J	70 fs Max.	70 fs Max.	60 fs Max.	156 MHz < fo ≤ 212 MHz		
		60 fs Max.	60 fs Max.	50 fs Max.	212 MHz < fo ≤ 391 MHz		
		50 fs Max.	60 fs Max.	50 fs Max.	391 MHz < fo ≤ 500 MHz	12 kHz to 20 MHz	

Product Name (Standard form)

SG2520 EGN 156.250000MHz C D H P Z A 456789

3

①Model ②Output (E: LV-PECL, V: LVDS) ③Frequency ④Supply voltage ⑤Frequency tolerance ®Operating temperature ⑦Function ®Output disable type (Z: High impedance) ®Output option

(4)	Supply voltage		
C	3.3 V Typ.		
D	2.5 V Typ.		
E*	1.8 V Typ.		
* IEI : 1 / 000 F00 1/01/			

(5)	⑤Freq. tolerance		
D	±25 × 10 ⁻⁶		
Е	±30 × 10 ⁻⁶		
っ	±50 × 10 ⁻⁶		

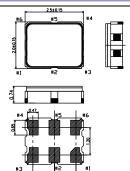
_	
6	Operating temp.
G	-40 °C to +85 °C
Н	-40 °C to +105 °C

	7	
0	unction	
Р	OE	
S	SŢ	

(Unit:mm)

90	Output option		
	SG2520EGN	SG2520VGN	
Α	Default	L_LVDS = 100 Ω, V_{OD} = 250 mV to 450 mV	
В	-	$L_LVDS = 50 \Omega$, $V_{OD} = 200 \text{ mV}$ to 500 mV	
С	-	L LVDS = 100Ω , $V_{OD} = 300 \text{ mV}$ to 600 mV	

External dimensions



Pin map				
	Pin	Connection		
	1	OE		
	2	N.C. (Open or Vcc)		
	3	GND		
	4	OUT		
	5	OUT		
	6	Vcc		

OE pin = HIGH or "Open": Specified frequency output. OE pin = LOW: Output is high impedance

Footprint (Recommended) (Unit:mm) 0.995 In order to achieve optimum jitter performance, it is

recommended that 0.1 µF and 10 µF bypass capacitors should be connected between V_{CC} and GND and placed as close to the V_{CC} pin as possible.

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

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IATF 16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

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►Pb free.



► Complies with EU RoHS directive.

*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.

(Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



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